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PATENT

JUL 0 8 2002 &

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bruce R. Parnas

Group Art Unit

Serial No.

10/089,137

Filed

March 23, 2002

For

TEST LANGUAGE CONVERSION

METHOD

Examiner

Unknown

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks Washington, D.C. 20231

Dear Sir:

Enclosed is a form PTO-1449 listing four references relevant to the above-identified application. A copy of each reference is also enclosed. This information disclosure statement is filed before the first office action on the merits under 37 CFR 1.97(b)(3), and thus no fee is required in accordance with 37 C.F.R. § 1.97(b)(1). The prior art references enclosed herewith are:

- (1) U.S. Patent No. 5,845,234 entitled "System and method for efficiently generating testing program code for use in automatic test equipment" and issued December 1, 1998.
- (2) U.S. Patent No. 5,778,004 entitled "Vector translator" and issued July 7, 1998.

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(3) U.S. Patent No. 6,067,652 "Tester-compatible timing translation system and method using time-set partnering" and issued May 23, 2000.

(4) U.S. Patent No. 5,974,241 "Test bench interface generator for tester compatible simulators" and issued October 26, 1999.

REMARKS

Applicant respectfully requests that the references disclosed in the statement be considered in the substantive examination of this case.

Respectfully submitted,
MURAMATSU & ASSOCIATES

Dated: 6/28/2002

By: Jeros

Yasuo Muramatsu Registration No. 38,684

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